

REMARKS

Applicants would like to thank the Examiner for the reviewing of this application. Based on a careful review of the cited references, Applicants submit that all the claims, as amended herein, are distinguishable from the prior art of record for at least the following reasons.

Claims 1-8, 15-20 and 22-25 stand rejected under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,768,482 (“*Asano*”). Applicants respectfully submit that the claims as amended herein overcome the rejection.

Amended claims 1 and 15 recite, *inter alia*, that the entire display areas of two light-emitting devices or color subpixels adjacent to each other in one row are arranged at alternated sides of the scan line which they are coupled with. Figure 2B of the application describes an embodiment of this feature:

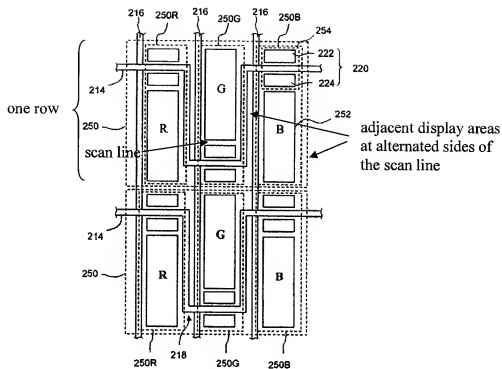


FIG. 2B

Asano fails to teach or even suggest a pixel array in which adjacent display areas in a same row are entirely arranged at alternated sides of the scan line they respectively coupled with, as claims 1 and 15 require. In contrast, FIG. 17 of *Asano* teaches a pixel array in which all the display areas in a same row are arranged on same side of the scan line. For at least the foregoing reasons, Applicants respectfully submit that amended claims 1 and 15 patently distinguish from the reference of *Asano*. At least by virtue of their respective dependency on claims 1 and 15, claims 2, 4, 6-8 and claims 16, 18 and 20-25 should also be patentable over *Asano*.

With respect to dependent claims 2 and 16, the claimed invention further recites that adjacent display areas in one row are oppositely offset relative to the row direction. *Asano* neither discloses nor suggests such feature of the claimed invention. In contrast,

Asano draw the light-emitting regions (dotted lines) in a same row at aligned positions. See FIGs. 6C, 9C, 10, and 12-14. The features specified in dependent claims 2 and 16 thus *per se* distinguish from the cited reference of *Asano*. Accordingly, withdrawal of the rejection is respectfully requested.

The Office Action also rejects claims 9-11, 14-17, 20, 21 and 25 under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,833,890 ("*Hong*"). Applicants respectfully submit that the claim amendments presented herein overcome the rejection.

Amended claim 9 recites, *inter alia*, that two display electrodes adjacent to each other in one row are entirely arranged at alternated sides of the scan line they coupled with. Like *Asano*, *Hong* neither teaches nor suggests such arrangement scheme of the display electrodes. For at least the foregoing reasons, claim 9 patently distinguishes from *Hong*. At least by virtue of their respective dependency on claim 9, claims 10, 12 and 14 are also patentable over the cited reference of *Hong*. Withdrawal of the rejection thus is respectfully requested.

Lastly, the Office Action rejects claims 12-13 under 35 U.S.C. §103(a) as being unpatentable over *Hong* in view of *Asano*. In rejecting claims 12-13, the Office Action admits that *Hong* fails to teach a scan line with a crenelated profile. However, the Office Action states that the teachings of *Asano* in conjunction with FIG. 14 disclose a modification where a scan line is crenelated and light-emitting regions are located alternately at two sides of the scan line. Thus, the Office Action concludes that it would have been obvious to combine the crenelated scan line as taught in *Asano* with the teachings of *Hong* to reach the claimed invention, which it does not.

Asano does not teach anywhere that light-emitting regions adjacent in one row are arranged at alternated sides of a crenelated scan line. In contrast, the teachings of *Asano* only teaches the positions where the contact portions 39 should be placed, which is not relevant to the configuration of the scan line that is in the sub-pixel circuit layer.

“The above configuration and its modification specify only the position of the contact portion 39, and do not specifically limit the layout of circuit devices such as TFTs forming the sub-pixel circuits.” (Emphasis highlighted) *Asano* col. 9, lines 29-32

In other words, the teachings of *Asano* do not disclose any crenelated profile of the scan line or arrangement of the display areas at alternated sides of the scan line, as claim 12 require. Accordingly, either alone or combined each other, neither *Asano* nor *Hong* adequately meet the features recited in the claimed invention. For at least the foregoing reasons, Applicants respectfully submit that claim 12 patently distinguish from *Asano* and *Hong*. Withdrawal of the rejection thus is respectfully requested.

CONCLUSION

Applicants believe that given the above amendments and remarks, all the pending claims are now in condition for allowance and such is respectfully requested. No new claim fees are believed to be necessitated by this response. The Examiner is requested to charge any additional fees that may due with this response to Deposit Account No. 02-0400 (BAKER & McKENZIE). If further discussion of this amendment is required, the Examiner is urged to contact the undersigned at the telephone numbers indicated below.

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Respectfully submitted,



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